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E. J. [unclear]  
9-27-02

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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*C. Healion*

C. Healion

Applicant : Janardhanan S. Ajit  
Application No. : 10/043,763  
Filed : January 9, 2002  
Title : SUB-MICRON HIGH INPUT VOLTAGE TOLERANT  
INPUT OUTPUT (I/O) CIRCUIT WHICH  
ACCOMMODATES LARGE POWER SUPPLY  
VARIATIONS  
Grp./Div. : 2858  
Examiner : Not Assigned  
Docket No. : 41980/RJP/B600

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INFORMATION DISCLOSURE STATEMENT  
37 CFR § 1.97(b)

Assistant Commissioner for Patents  
Washington, D.C. 20231

Post Office Box 7068  
Pasadena, CA 91109-7068  
July 30, 2002

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, and in accordance with the provisions in the Manual of Patent Examining Procedure §§ 609 and 707.05(b), enclosed is FORM PTO/SB/08A/B listing the references that are known to applicant. Copies of each of the listed references are enclosed. This filing is timely because it is made during one of the periods described in 37 CFR § 1.97(b).

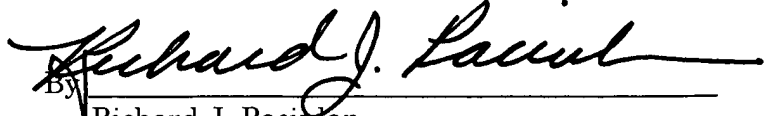
It is respectfully requested that the listed references be considered in the examination of this application and identified on the list of references cited on the patent issuing for this application. Applicant also requests that an initialed copy of FORM PTO/SB/08A/B be entered

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in the application file and returned to applicant with the next communication from the Office in accordance with MPEP § 609.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

A handwritten signature in black ink, reading "Richard J. Paciulan", written over a horizontal line.

By Richard J. Paciulan

Reg. No. 28,248

626/795-9900

RJP/cah

Enclosures: PTO/SB/08A/B, w/references

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